

## Faculty Profile

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| Name   | Dr. Nagavalli Vegesna  |
| Date of Birth  | 26/02/1988   |
| Unique ID (AICTE ID)   | 1-3206192760   |
| Education Qualifications   | M. Tech, Ph.D  |
| Work Experience  |  |
| a) Teaching  | 12Years  |
| b) Research  | 5 Years  |
| c) Industry  |  |
| d) Others  |  |
| Area of Specialization   | Array Antennas, VLSI Design  |
| Courses taught at Under Graduate/ Post Graduate Level  | <b>Post Graduate Level</b><br>Digital IC Design<br><b>Under Graduate Level</b><br>Linear Integrated Circuits and Applications<br>Electronic Circuit Analysis<br>Analog Electronics<br>Micro Electronics<br>Radar Engineering<br>Basic Electronics<br>Switching Theory and Logic Design<br>Design for Testability |
| Research guidance  |  |
| a. No. of papers published in National/ International Journals/ Conferences                    | Nil  |
| b. Master (No. of Completed/Ongoing)   | 02   |
| c. Ph.D. (No. of Completed/Ongoing)  | Nil  |
| No. of Projects Carried out  | Nil  |
| No. of Patents (Filed & Granted)   | 02   |
| Technology Transfer  |  |
| Research Publications (No. of papers published in National/International Journals/Conferences) | 23   |
| No. of Books published   | Nil  |