

Faculty Profile

Name	KALIDINDI LAKSHMI DIVYA
Date of Birth	02-11-1990
Unique ID (AICTE ID)	1-3206192201
Education Qualifications	MTECH
Work Experience	
a) Teaching	7 YEARS
b) Research	2 YEARS
c) Industry	-
d) Others	-
Area of Specialization	VLSI DESIGN
Courses taught at Under Graduate/ Post Graduate Level	VLSI DESIGN, ANALOG ELECTRONICS,PULSE AND DIGITAL CIRCUITS, BASIC ELECTRONICS, DIGITAL SIGNAL PROCESSING, INTERNET OF THINGS, SOFT COMPUTING,CPLD & FPGA
Research guidance	-
a. No. of papers published in National/ International Journals/ Conferences	-
b. Master (No. of Completed/Ongoing)	-
c. Ph.D. (No. of Completed/Ongoing)	-
No. of Projects Carried out	-
No. of Patents (Filed & Granted)	1
Technology Transfer	-
Research Publications (No. of papers published in National/International Journals/Conferences)	9
No. of Books published	-