

## Faculty Profile

Name	D.BHAVANI
Date of Birth	01/02/1989
Unique ID (AICTE ID)	1-2657492077
Education Qualifications	M.TECH
Work Experience	8
a) Teaching	8
b) Research	-
c) Industry	-
d) Others	-
Area of Specialization	VLSI DESIGN
Courses taught at Under Graduate/ Post Graduate Level	ELECTRONC DEVICES AND CIRCUITS, RADAR ENGINEERING, DIGITAL IMAGE PROCESSING, VLSI DESIGN RF CIRCUIT DESIGN ANALOG CIRCUITS
Research guidance	
a. No. of papers published in National/ International Journals/ Conferences	8
b. Master (No. of Completed/Ongoing)	-
c. Ph.D. (No. of Completed/Ongoing)	-
No. of Projects Carried out	-
No. of Patents (Filed & Granted)	-
Technology Transfer	-
Research Publications (No. of papers published in National/International Journals/Conferences)	8
No. of Books published	-